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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa, Mark N. Yamashita and Yuval Bachar

Assignee: Cisco Technology, Inc.

Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK  
BETWEEN VIAS IN A PRINTED CIRCUIT BOARD

Application No.: 10/727,381 Filing Date: December 4, 2003

Examiner: Unassigned Group Art Unit: 2811

Docket No.: CIS0203US

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

POWER OF ATTORNEY  
BY ASSIGNEE OF ENTIRE INTEREST

Sir:

The above-identified assignee, a California corporation, hereby revokes all powers of attorney previously given and appoints the attorneys identified by the following attorneys to prosecute the above-identified application and to transact all business in the United States Patent and Trademark Office in connection therewith:

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**ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(B)**

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title and interest in the above-identified patent application by virtue of a chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee as shown below:

The document was recorded in the Patent and Trademark Office at Reel 014773, Frame 0852.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

**JUL 23 2004**

Date



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